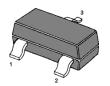


MMBTSC5084 NPN Silicon Epitaxial Planar Transistor

for low noise, high gain amplifier at VHF~UHF band.

The transistor is subdivided into two groups O and Y, according to its DC current gain.



1.Base 2.Emitter 3.Collector SOT-23 Plastic Package

Absolute Maximum Ratings (T_a = 25 °C)

	Symbol	Value	Unit
Collector Base Voltage	V _{CBO}	20	V
Collector Emitter Voltage	V _{CEO}	12	V
Emitter Base Voltage	V _{EBO}	3	V
Base Current	l _B	40	mA
Collector Current	Ic	80	mA
Power Dissipation	P _{tot}	200	mW
Junction Temperature	T _j	125	°C
Storage Temperature Range	Ts	-55 to +125	°C



Characteristics at T_{amb} =25 °C

	Symbol	Min.	Тур.	Max.	Unit
DC Current Gain					
at V _{CE} =10V, I _C =20mA					
Current Gain Group O	h _{FE}	80	-	160	-
Y	h _{FE}	120	-	240	-
Collector Cutoff Current					
at V _{CB} =10V	I _{CBO}	-	-	1	μΑ
Emitter Cutoff Current					
at V _{EB} =1.0V	I _{EBO}	-	-	1	μA
Transition Frequency					
at V _{CE} =10V, I _C =20mA	f _T	5	7	-	GHz
Reverse Transfer Capacitance					
at V _{CB} =10V, f=1MHz ¹⁾	C_{re}	-	0.65	1.15	pF
Output Capacitance					
at V _{CB} =10V, f=1MHz ¹⁾	C_ob	-	1	-	pF
Insertion Gain					
at V _{CE} =10V, I _C =20mA, f=500MHz	S _{21e} 2 1	-	16.5	-	dB
Insertion Gain					
at V _{CE} =10V, I _C =20mA, f=1.0GHz	$\left S_{21e} \right _{2}^{2}$	7.5	11	-	dB
Noise Figure					
at V _{CE} =10V, I _C =5mA, f=500MHz	NF ₁	-	1	-	dB
Noise Figure					
at V _{CE} =10V, I _C =5mA, f=1.0GHz	NF ₂	-	1.1	2	dB

 $^{^{\}mbox{\scriptsize 1)}}\,C_{\mbox{\scriptsize re}}$ is measured by 3 terminal method with capacitance bridge.



PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23

