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## BCW61 PNP Silicon Epitaxial Planar Transistors

for general purpose switching and amplification.
These transistors are subdivided into three groups B, C and D, according to their current gain.

As complementary types the NPN transistors BCW60 are recommended.

1.Base 2.Emitter 3.Collector SOT-23 Plastic Package

Absolute Maximum Ratings ( $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}$ )

| Parameter | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Collector-Base Voltage | $-\mathrm{V}_{\text {CBO }}$ | 32 | V |
| Collector-Emitter Voltage | $-\mathrm{V}_{\text {CEO }}$ | 32 | V |
| Emitter-Base Voltage | $-\mathrm{V}_{\text {EBO }}$ | 5 | V |
| Collector Current | $-\mathrm{I}_{\mathrm{C}}$ | 100 | mA |
| Peak Collector Current | $-\mathrm{I}_{\text {CM }}$ | 200 | mA |
| Peak Base Current | $-\mathrm{I}_{\text {BM }}$ | 100 | mA |
| Power Dissipation | $\mathrm{P}_{\text {tot }}$ | 200 | mW |
| Junction Temperature | $\mathrm{T}_{J}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\mathrm{S}}$ | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

Characteristics at $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| DC Current Gain <br> at $-\mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V},-\mathrm{I}_{\mathrm{C}}=10 \mu \mathrm{~A}$ <br> BCW61B <br> BCW61C <br> BCW61D <br> at $-V_{C E}=5 \mathrm{~V},-I_{C}=2 \mathrm{~mA}$ <br> BCW61B <br> BCW61C <br> BCW61D <br> at $-V_{C E}=1 \mathrm{~V},-I_{C}=50 \mathrm{~mA}$ <br> BCW61B <br> BCW61C <br> BCW61D | $h_{\text {FE }}$ <br> $h_{\text {FE }}$ <br> $h_{\text {FE }}$ <br> $h_{\text {FE }}$ <br> $h_{\text {FE }}$ <br> $h_{\text {FE }}$ <br> $h_{\text {FE }}$ <br> $h_{\text {FE }}$ <br> $h_{\text {FE }}$ | $\begin{gathered} 30 \\ 40 \\ 100 \\ 180 \\ 250 \\ 380 \\ 80 \\ 100 \\ 110 \end{gathered}$ |  | $\begin{aligned} & 310 \\ & 460 \\ & 630 \end{aligned}$ |  |
| Collector Saturation Voltage at $-I_{C}=10 \mathrm{~mA},-\mathrm{I}_{\mathrm{B}}=0.25 \mathrm{~mA}$ | $-V_{\text {CEsat }}$ | 0.06 | - | 0.25 | V |
| Collector Saturation Voltage at $-I_{C}=50 \mathrm{~mA},-I_{\mathrm{B}}=1.25 \mathrm{~mA}$ | $-V_{\text {CEsat }}$ | 0.12 | - | 0.55 | V |
| Base Saturation Voltage at $-I_{C}=10 \mathrm{~mA},-\mathrm{I}_{\mathrm{B}}=0.25 \mathrm{~mA}$ | $-V_{\text {BEsat }}$ | 0.6 | - | 0.85 | V |
| Base Saturation Voltage at $-I_{C}=50 \mathrm{~mA},-I_{B}=1.25 \mathrm{~mA}$ | - $\mathrm{V}_{\text {BEsat }}$ | 0.68 | - | 1.05 | V |
| Base-Emitter Voltage at $-I_{C}=2 \mathrm{~mA},-\mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V}$ | $-\mathrm{V}_{\mathrm{BE} \text { (on) }}$ | 0.6 | - | 0.75 | V |
| Collector Base Cutoff Current <br> at $-V_{C B}=32 \mathrm{~V}$ <br> at $-V_{C B}=32 \mathrm{~V}, \mathrm{~T}_{\mathrm{j}}=150^{\circ} \mathrm{C}$ | $\begin{aligned} & -I_{\mathrm{CBO}} \\ & -\mathrm{I}_{\mathrm{CBO}} \end{aligned}$ |  | - | $\begin{aligned} & 20 \\ & 20 \end{aligned}$ | $\begin{aligned} & \mathrm{nA} \\ & \mu \mathrm{~A} \end{aligned}$ |
| Emitter-Base Cutoff Current at $-V_{E B}=4 V$ | $\mathrm{l}_{\text {Ebo }}$ | - | - | 20 | nA |
| Gain -Bandwidth Product at $-\mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V},-\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{f}=100 \mathrm{MHz}$ | $\mathrm{f}_{\mathrm{T}}$ | 100 | - | - | MHz |
| Collector-Base Capacitance at $-\mathrm{V}_{\mathrm{CB}}=10 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\text {CBO }}$ | - | 4.5 | - | pF |
| Emitter-Base Capacitance at $-\mathrm{V}_{\mathrm{EB}}=0.5 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\text {Ebo }}$ | - | 11 | - | pF |
| Noise figure <br> at $-\mathrm{I}_{\mathrm{C}}=200 \mu \mathrm{~A},-\mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V}, \mathrm{R}_{\mathrm{S}}=2 \mathrm{~K} \Omega, \mathrm{f}=1 \mathrm{KHz}, \Delta \mathrm{f}=200 \mathrm{~Hz}$ | NF | - | 2 | 6 | dB |
| Thermal Resistance, Junction to Ambient | $\mathrm{R}_{\theta \mathrm{JA}}$ | - | - | $500{ }^{1)}$ | K/W |

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## PACKAGE OUTLINE

Plastic surface mounted package; 3 leads



[^0]:    ${ }^{1)}$ Transistor mounted on an FR4 printed-circuit board.

