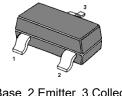


BCW61 PNP Silicon Epitaxial Planar Transistors

for general purpose switching and amplification.

These transistors are subdivided into three groups B, C and D, according to their current gain.

As complementary types the NPN transistors BCW60 are recommended.



1.Base 2.Emitter 3.Collector SOT-23 Plastic Package

Absolute Maximum Ratings (T_a = 25 °C)

Parameter	Symbol	Value	Unit	
Collector-Base Voltage	-V _{CBO}	32	V	
Collector-Emitter Voltage	-V _{CEO}	32	V	
Emitter-Base Voltage	-V _{EBO}	5	V	
Collector Current	-I _C	100	mA	
Peak Collector Current	-I _{CM}	200	mA	
Peak Base Current	-I _{BM}	100	mA	
Power Dissipation	P _{tot}	200	mW	
Junction Temperature	TJ	150	°C	
Storage Temperature Range	T _S	-65 to +150	°C	



Characteristics at $T_a = 25$ °C

Parameter		Symbol	Min.	Тур.	Max.	Unit
DC Current Gain						
at -V _{CE} = 5 V, -I _C = 10 μ A	3CW61B	h_{FE}	30	-	-	-
В	CW61C	h_{FE}	40	-	-	-
В	CW61D	h_{FE}	100	-	-	-
at -V _{CE} = 5 V, -I _C = 2 mA	3CW61B	h_{FE}	180	-	310	-
В	CW61C	h_{FE}	250	-	460	-
В	CW61D	h_{FE}	380	-	630	-
at -V _{CE} = 1 V, -I _C = 50 mA	3CW61B	h_{FE}	80	-	-	-
В	CW61C	h_{FE}	100	-	-	-
В	CW61D	h _{FE}	110	-	-	-
Collector Saturation Voltage		-V _{CEsat}	0.06		0.25	V
at $-I_C = 10 \text{ mA}$, $-I_B = 0.25 \text{ mA}$		- V CEsat	0.06	-	0.25	
Collector Saturation Voltage		-V _{CEsat}	0.12	-	0.55	V
at $-I_C = 50 \text{ mA}$, $-I_B = 1.25 \text{ mA}$		- V CEsat				
Base Saturation Voltage		-V _{BEsat}	0.6	_	0.85	V
at $-I_C = 10 \text{ mA}, -I_B = 0.25 \text{ mA}$		▼ BESat	0.0		0.00	
Base Saturation Voltage		-V _{BEsat}	0.68	_	1.05	V
at $-I_C = 50 \text{ mA}$, $-I_B = 1.25 \text{ mA}$		- DESat				
Base-Emitter Voltage		-V _{BE(on)}	0.6	_	0.75	V
at $-I_C = 2 \text{ mA}$, $-V_{CE} = 5 \text{ V}$		- BE(OII)				
Collector Base Cutoff Current		-I _{CBO}	_	_	20	nA
at -V _{CB} = 32 V		-I _{CBO}	_	_	20	μA
at -V _{CB} = 32 V, T _j = 150 °C		-iCBO			20	μ/ (
Emitter-Base Cutoff Current		l	-	-	20	nA
at -V _{EB} = 4 V		-I _{EBO}				
Gain -Bandwidth Product		f⊤	100	-	_	MHz
at $-V_{CE} = 5 \text{ V}$, $-I_{C} = 10 \text{ mA}$, $f = 100 \text{ MHz}$		11	100	1	_	IVII IZ
Collector-Base Capacitance		C_{CBO}	_	4.5	_	pF
at -V _{CB} = 10 V, f = 1 MHz		ОСВО	_	٦.٥	_	ρı
Emitter-Base Capacitance		C_{EBO}		11		pF
at -V _{EB} = 0.5 V, f = 1 MHz		OEBO	_	11	_	ρι
Noise figure		NF		2	6	dB
at $-I_C = 200 \mu A$, $-V_{CE} = 5 V$, $R_S = 2 K\Omega$, $f = 1 KHz$,	Δf=200Hz	141	_			40
Thermal Resistance, Junction to Ambient		$R_{\theta JA}$	-	-	500 ¹⁾	K/W

¹⁾Transistor mounted on an FR4 printed-circuit board.



PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23

